

AMENDMENTS TO THE CLAIMS:

Complete Listing of Claims

1. (currently amended) A digital amplifier including a noise shaper and a dither generator arranged to introduce noise to the shaper, said generator using a seed value derived from a state variable ~~programmable delay element~~ of said shaper.
2. (original) A digital amplifier as claimed in claim 1 and wherein the number of bits in the generated noise exceeds that of the seed value.
3. (original) A digital amplifier as claimed in Claim 1 or Claim 2 and wherein the dither generator includes shift registers of predetermined bit lengths to receive said seed values and provide a noise output.
4. (original) A digital amplifier as claimed in any preceding claim including means for scaling said noise.

Claims 5-23. (canceled)